

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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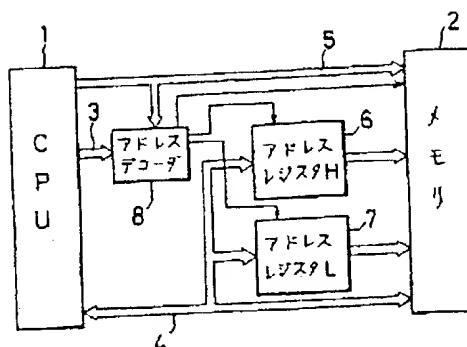
APPLICATION DATE : 16-06-87  
APPLICATION NUMBER : 62148003

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INVENTOR : YOSHIDA KOICHI;

INT.CL. : G06F 12/06 G06F 13/16

TITLE : ADDRESSING CIRCUIT



ABSTRACT : PURPOSE: To expand an address space to be accessed by writing data on a data bus in an address register connected to the data bus and using the data as an address of a device to be accessed.

CONSTITUTION: In case of accessing a memory 2 from a CPU1, a memory address to be accessed is written in address registers H6, L7 set up by an address decoder 8 as data from the data bus 4 prior to the memory access. When the CPU1 outputs an active signal from the data bus 4, the data stored in the registers H6, L7 are transferred to the memory 2 as the address data of the memory 2 and data are transmitted/received between the memory 2 and the CPU1 in accordance with a signal from a control bus 5.

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